

2023 STUDENT EMC HARDWARE DESIGN COMPETITION

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SCHEDULE

- Enrollment period opens: March 1, 2023
- Deadline to submit videos: March 10, 2023
- Notification of Top 3 finalists: May 10, 2023 (Top 3 finalists will be listed in the conference program)
- Winning entries will be recognized at the Awards Luncheon IEEE EMC+SIPI 2023: August 3, 2023

The **Education Committee (EdCom) of the IEEE EMC Society** would like to invite students currently enrolled in a college or university at either the Ph.D., graduate, or undergraduate level to submit a high-quality short video (max 10 minutes) that demonstrates an **Innovative Hardware-Based Design Solution for Electromagnetic Compatibility (EMC), Signal & Power Integrity (SIPI), or Automotive Applications in these areas**. Submitted videos should be both entertaining and educational. They should be hardware demonstrations, not software simulations. The competition is designed to provide an opportunity for student competitors to showcase their creative design skills while informing other students or the general public about a topic relevant to EMC or SIPI. Videos should be designed and produced by a student or a small group of students working together. Advisors should limit their help to answering basic questions from the students once the project has begun.

GENERAL COMPETITION RULES

1. The Team should include a mentor (i.e., academic advisor, industry professional, etc.). Entries are limited to one team per mentor. Student groups are expected to complete the work on their own.
2. The student(s) should use the corresponding institutional email address rather than their personal emails for all correspondence.
3. The Top 3 finalists will do the second round of the Hardware demonstration on site at the symposium venue.
 - First Place: \$2,000 USD
 - Second Place: \$1,000 USD
 - Third Place: \$500 USD
4. Award recipients will need to complete the appropriate US Department of the Treasury Internal Revenue Service forms as required by the IEEE.
5. The winning team should submit a brief article to the IEEE EMC Magazine to share their design experience with the greater EMC and SIPI Community.

COMPETITION COORDINATORS

Pavithrkrishnan Radhakrishnan (Chair)
Mohammad Kameli
Zhao Chen

ESAT-WaveCore, M-Group
KU Leuven Bruges Campus, Belgium

COMPETITION INSTRUCTIONS

Submit your **YouTube video link** where the video can be viewed to:

Pavithrkrishnan Radhakrishnan
(pavithrkrishnan.radhakrishnan@ieee.org)

The submitter will receive a confirmation email after a successful submission.

Submissions should include the following contents:

1. The video (link), including the names of all team members and their institutional email addresses, along with the mentor's name, title, and email address. The title, team members, mentor name, and institution name should be at the beginning of the video with the institutional background or logo.
2. A mentor letter is **mandatory**, stating that the video is the original work of the students.

Submission period: from **March 1, 2023** to **March 10, 2023**.

DEADLINE EXTENDED: APRIL 22, 2023

Evaluation Criteria: All submittals will be judged by a team comprised of several EMC Society EdCom members based on technical design (35%), creativity (35%), and educational merit (30%).

Winner of the 2022 Student EMC Hardware Design Contest
Lukasz Nowarkiewicz & Team Poznan University of Technology,
Poland (with IEEE EMCS President, Symposium Chair, and Student HW
Design Contest Chair during the 2022 IEEE EMC+SIPI Symposium)

